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FACSIMILE COVER SHEET

To Office of Initial Patent Examination's
Filing Receipt Corrections

Total Pages Sent: 8
(including cover sheet)

Facsimile Number: 703-746-9195

Transmission Date: December 15, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brintzinger, et al.

Docket No.: 2003 P 51167 US

Serial No: 10/826,603

Art Unit: 2812

Date Filed: April 16, 2004

Title: Method for Protecting the Redistribution Layer on Wafers/Chips

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-746-9195 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Request for Corrected Filing Receipt (1 page)
- Marked-Up Copy of Filing Receipt (2 pages)
- Copy of Fully Executed Declaration and Power of Attorney (3 pages)
- Change of Correspondence Address (1 page)

Respectfully submitted,



Natalie Swider
Legal Assistant

Confirmation Respectfully Requested

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brintzinger, et al. Docket No.: 2003 P 51167 US
Serial No.: 10/826,603 Art Unit: 2812
Filed: April 16, 2004 Examiner: TBD
For: Method for Protecting the Redistribution Layer on Wafers/Chips

Office of Initial Patent Examination's
Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Request for Corrected Filing Receipt

Dear Sir:

Upon review of the Official Filing Receipt mailed August 30, 2004, for the above-referenced patent application, Applicants have noted an error in the Power of Attorney section and hereby request correction of the error. Specifically, as shown on page 2 of the attached copy of the fully executed Declaration and Power of Attorney, power of attorney was granted to all practitioners at Customer No. 25962. A marked-up copy of the Filing Receipt showing the requested change is also attached hereto.

In addition, Applicants respectfully request that the Office make of record the attached Change of Correspondence Address.

Please contact Applicants' attorney with any questions regarding this matter.

Respectfully submitted,



Ira S. Matsil
Reg. No. 35,272
Attorney for Applicants

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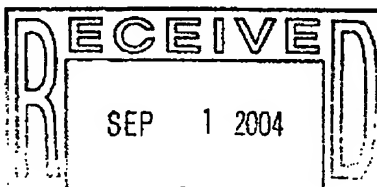


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/826,603	04/16/2004	2812	1008	2003 P 51167 US	2	26	3

25962
SLATER & MATSIL, L.L.P.
17950 PRESTON RD, SUITE 1000
DALLAS, TX 75252-5793



CONFIRMATION NO. 1195

UPDATED FILING RECEIPT



OC000000013672947

Date Mailed: 08/30/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Axel Brintzinger, Dresden, GERMANY;
Octavio Trovarelli, Dresden, GERMANY;

Power of Attorney: None

The practitioners associated with Customer Number 25962.

Domestic Priority data as claimed by applicant

Foreign Applications

GERMANY 103 18 078.8 04/17/2003

If Required, Foreign Filing License Granted: 08/25/2004

The number of your priority application, to be used for filing abroad under the Paris Convention is,
US10/826,603

Projected Publication Date: 12/09/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method for protecting the redistribution layer on wafers/chips

Preliminary Class

438

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